

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3106	(calculat\$4 near\$5 metric\$1)	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/04/07 11:18
L3	21	(calculat\$4 or perform\$4 or comput\$5) same ((forward adj3 metric\$1) same (reverse adj2 metric\$1))	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/04/07 11:20
L4	21	(calculat\$4 or perform\$4 or comput\$5 or stor\$4 or read\$4 or writ\$4) same ((forward adj3 metric\$1) same (reverse adj2 metric\$1))	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/04/07 11:21
L5	20	((calculat\$4 or perform\$4 or comput\$5 or stor\$4 or read\$4 or writ\$4) same ((forward adj3 metric\$1) same (reverse adj2 metric\$1))) and decod\$4	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/04/07 11:22
L6	3	((calculat\$4 or perform\$4 or comput\$5 or stor\$4 or read\$4 or writ\$4) same ((forward adj3 metric\$1) same (reverse adj2 metric\$1))) and decod\$4 and binary	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/04/07 11:23
L7	7	((calculat\$4 or perform\$4 or comput\$5 or stor\$4 or read\$4 or writ\$4) and ((forward adj3 metric\$1) and (reverse adj2 metric\$1))) and decod\$4 and binary	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/04/07 11:24
L8	6	((calculat\$4 or perform\$4 or comput\$5 or stor\$4 or read\$4 or writ\$4) and ((forward adj3 metric\$1) and (reverse adj2 metric\$1))) and decod\$4 and binary and state\$1	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/04/07 11:24
L9	6	((calculat\$4 or perform\$4 or comput\$5 or stor\$4 or read\$4 or writ\$4) and ((forward adj3 metric\$1) and (reverse adj2 metric\$1))) and decod\$4 and binary and (state\$1 or stage\$1)	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/04/07 11:42
L10	5	(calculat\$4 or perform\$4 or comput\$5 or stor\$4 or read\$4 or writ\$4) and (((forward adj3 metric\$1 adj2 value\$1) and (reverse adj2 metric\$1 adj2 value\$1))	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/04/07 11:43

L11	3	(calculat\$4 or perform\$4 or comput\$5 or stor\$4 or read\$4 or writ\$4) and ((forward adj2 metric\$1 adj2 value\$1) and (reverse adj2 metric\$1 adj2 value\$1))	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/04/07 11:44
L12	16	(calculat\$4 or perform\$4 or comput\$5 or stor\$4 or read\$4 or writ\$4) same ((forward adj2 metric\$1) same (reverse adj2 metric\$1))	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/04/07 11:44
L13	23	(calculat\$4 or perform\$4 or comput\$5 or stor\$4 or read\$4 or writ\$4) same ((forward\$3 near2 metric\$1) same (revers\$3 near2 metric\$1))	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/04/07 11:45
L14	21	((calculat\$4 or perform\$4 or comput\$5 or stor\$4 or read\$4 or writ\$4) same ((forward\$3 near2 metric\$1) same (revers\$3 near2 metric\$1))) and decod\$4	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/04/07 11:46
L15	10	((calculat\$4 or perform\$4 or comput\$5 or stor\$4 or read\$4 or writ\$4) same ((forward\$3 near2 metric\$1) same (revers\$3 near2 metric\$1))) and decod\$4 and extrinsic	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/04/07 11:48
L16	10	((calculat\$4 or perform\$4 or comput\$5 or stor\$4 or read\$4 or writ\$4) same ((forward\$3 near2 metric\$1) same (revers\$3 near2 metric\$1))) and (turbo near2 decod\$3) and extrinsic	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/04/07 11:49
L17	41	((calculat\$4 or perform\$4 or comput\$5 or stor\$4 or read\$4 or writ\$4) same (((forward\$3 near2 metric\$1) or alpha) same (revers\$3 near2 metric\$1) or beta)) and (turbo near2 decod\$3) and extrinsic	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/04/07 11:50


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IEEE CNF IEEE Conference Proceeding

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IEEE CNF IEEE Conference Proceeding

IEEE STD IEEE Standard

**1. A low-complexity iterative multiuser receiver for turbo-coded DS-CDMA systems**

Jah-Ming Hsu; Chin-Liang Wang;

Selected Areas in Communications, IEEE Journal on

Volume 19, Issue 9, Sept. 2001 Page(s):1775 - 1783

Summary: Optimal joint multiuser detection and decoding for direct-sequence code-division multiple-access with forward error correction normally requires prohibitively high computational complexity. A suboptimal solution with complexity

[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(208 KB) IEEE JNL**2. Modification of branch metric calculation to improve iterative SOVA decoding of turbo codes**

Papaharalabos, S.; Sweeney, P.; Evans, B.G.;

Electronics Letters

Volume 39, Issue 19, 18 Sept. 2003 Page(s):1391 - 1392

Summary: It is known that the performance of a SOVA (soft output Viterbi algorithm) turbo decoder can be improved by using extrinsic information that is produced at its output is over-optimistic. A new parameter associated with the branch metric is proposed.

[AbstractPlus](#) | Full Text: [PDF](#)(193 KB) IEEE JNL**3. A low-complexity iterative multiuser receiver for turbo-coded DS-CDMA systems**

Jah-Ming Hsu; Chin-Liang Wang;

Communications, 2000. ICC 2000. 2000 IEEE International Conference on

Volume 3, 18-22 June 2000 Page(s):1218 - 1222 vol.3

Summary: We propose a low-complexity iterative multiuser receiver for turbo-coded DS-CDMA systems. The multiuser receiver consists of a modified decorrelating decision-feedback detector (MDDFD) and K single-user receivers where K is the number of users.

[AbstractPlus](#) | Full Text: [PDF](#)(368 KB) IEEE CNF**4. Simplified recursive structure for turbo decoder with Log-MAP algorithm**

Chunlong Bai; Jun Jiang; Ping Zhang;

Vehicular Technology Conference, 2002. VTC Spring 2002. IEEE 55th

Volume 2, 6-9 May 2002 Page(s):1012 - 1015 vol.2

Summary: For the efficient implementation of a turbo decoder with Log-MAP (logarithm-maximum a posteriori) algorithm, we propose in this paper a solution with three highlights: the general core for forward and backward recursions, the simplified branch metric calculation.

[AbstractPlus](#) | Full Text: [PDF](#)(282 KB) IEEE CNF


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 Pietrobon, S.S.;
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 Volume 1, 9-12 Sept. 1997 Page(s):427 - 431 vol.1
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- ☐ 2. **An efficient turbo decoder architecture for IMT2000**
 In San Jeon; Bong Seop Song; Kyung Soo Kim; Han Jin Cho; Whan Woo Kim;
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 Atturi, I.; Arslan, T.;
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 Jaeyoung Kwak; Sook Min Park; Kwyro Lee;
 Circuits and Systems, 2003. ISCAS '03. Proceedings of the 2003 International Symposium on
 Volume 2, 25-28 May 2003 Page(s):II-280 - II-283 vol.2
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- ☐ 5. **Reconfigurability-power trade-offs in turbo decoder design and implementation**
 Atturi, I.; Arslan, T.;
 VLSI, 2004. Proceedings. IEEE Computer society Annual Symposium on
 19-20 Feb. 2004 Page(s):215 - 217
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